Integrated Distributed Controller

Network intelligence at the door

The Integrated Distributed Controller (IDC) is the most comprehensive controller in Quintron's latest generation of intelligent controller boards. It is ideal for small sites, remote locations, or special secure area applications.

In addition to controlling and monitoring up to 32 sub-controllers (SRI, DRI, ERI, IP, OP or KRI), the IDC also provides the on-board I/O necessary for controlling two doors. Readers, keypads, and readers with integrated keypads that use Wiegand, clock-and-data, or RS-485 signaling are supported. Inputs and outputs can be assigned to door-related functions or as general purpose I/O. Inputs can be unsupervised or supervised using configurable end-of-line resistance values. Outputs are implemented using Form-C (NO/NC) relays and can be used for fail-safe or fail-secure lock operation. The IDC is capable of storing close to 400,000 card records when using a minimum configuration. A vast array of access control and intrusion detection scenarios can be implemented using the IDC's ability to cause a series of complex actions to occur on a sub-controller based on a time schedule, an event trigger (originating from the same or another sub-controller), or operator control. The IDC connects to its sub-controllers over a two wire RS-485 bus that can be extended up to 4,000 feet.

The IDC is network ready and is managed by a host computer running industry-leading AccessNsite® software. The host computer downloads credential, command, and configuration data to the IDC, and uploads event and status change information to display at an operator's workstation. Once the IDC has been configured, it can run independently should it lose its connection with the host computer. If it loses its host connection, the IDC stores events in battery-backed RAM, which will be uploaded to the host once the connection is restored.

Features

- Most widely accepted access control hardware due to its reliability and open architecture
- Controls two doors
- Up to 50,000 event buffer
- 64-bit max card number
- 390,000 card holders (max); 240,000 card holders (typical)
- 15 digit max PIN
- PIV-II, CAC, TWIC compatible
- AES 128 bit encryption
- Support for Holidays and Daylight Saving Time
- Supports up to 8 programmable card formats
- 12V to 24V DC operation
- Tamper and power fault monitor inputs
- Field upgradable firmware
- UL listed 294/1076
- 1 year limited warranty

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IDC Specifications

Primary Power
- 12 VDC to 24 VDC +/- 10%, 500 mA maximum (reader current not included)

Communications
- Host Port 0: 10/100 Ethernet
- Host Port 1: RS-232
- Peripheral Port: 2-wire RS-485

Reader Interface
- Unregulated pass through or regulated 12 VDC; (150 mA maximum)
- One or two wire bi-color LED support
- Buzzer (Only with ‘one-wire’LED)
- Clock-and-Data, Wiegand, or 2-wire RS-485 signaling

Inputs
- 8 supervised
- 2 dedicated: Tamper & Power Monitor

Outputs
- 4 Relays: Form-C, 5 A, 30 VDC

Dimensions
- 8.0” W x 6.0” L x 1.0” H

Temperature
- 0(°)C to 70(°)C operating
- -55(°)C to 85(°)C storage

Humidity
- 0% to 95% RHNC

Certifications
- UL294, UL1076
- CE
- RoHS
- FCC Part 15 Class A
- NIST Certified Encryption

* For a list of preconfigured enclosures, see Quintron’s Enclosures brochure.